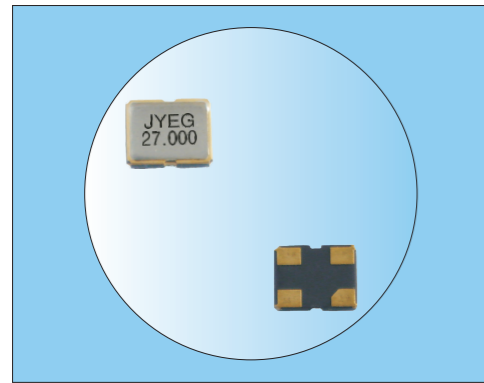


# Crystal Clock Oscillators

## Clock Oscillator/SMD 2520



### Features

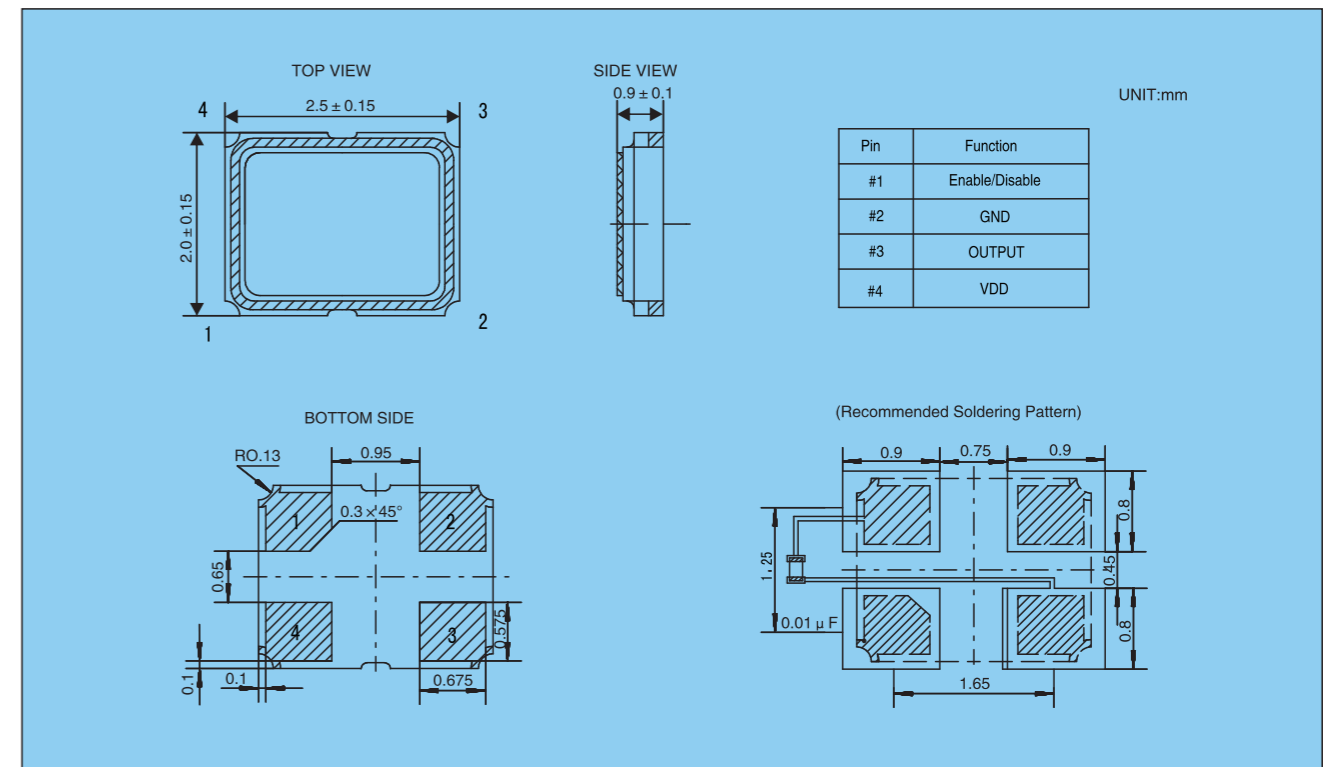
- JYEG part number :JYOD2
- 3.3V、2.5V、1.8V supply voltage
- CMOS/TTL output
- Tri-state function available
- Tape and reel available

### TYPICAL SPECIFICATIONS

Type	OSC SMD2520	
Frequency Range	32.768KHZ, 1.000MHZ~170.000MHZ(AT1)	
Frequency Stability	$\geq \pm 25\text{ppm}/\geq \pm 30\text{ppm}/\geq \pm 50\text{ppm}/\geq \pm 100\text{ppm}$	
Operating Temperature Range	$-20^{\circ}\text{C}\sim 70^{\circ}\text{C}/-40^{\circ}\text{C}\sim 85^{\circ}\text{C}/-40^{\circ}\text{C}\sim 105^{\circ}\text{C}/-40^{\circ}\text{C}\sim 125^{\circ}\text{C}$	
Output Load	1~5TTL or CMOS 30PF Max	
Input Current	32.768KHZ	$\leq 0.5\text{mA}$
	$\leq 35\text{MHZ}$	$\leq 8\text{mA}$
	35~ $\leq 50\text{MHZ}$	$\leq 16\text{mA}$
	$>50\text{MHZ}$	$\leq 25\text{mA}$
Supply Voltage	$3.3\text{V} \pm 10\%/2.5\text{V} \pm 10\%/1.8\text{V} \pm 10\%$	
Start-up Time	1ms Max	
Duty Cycle	40%~60% Normal,45%~55% Tight	
Rise/Fall Time	5ns Max	
Output "0" Level	TTL	CMOS
	0.4V Max	10%VDD Max
Output "1" Level	TTL	CMOS
	2.4V Min	90%VDD Min
Tri-state	Pin1:0.7 VDD Min(High)or open,Output:Enable	
	Pin1:0.3 VDD Max(Low),Output:Disable	
Jitter(12KHZ~20MHZ)	1 ps Max	
Aging	$\pm 3\text{ppm}/\text{year}$ Max	
Storage Temperature Range	$-55^{\circ}\text{C}\sim 125^{\circ}\text{C}$	

# Crystals Clock Oscillators

## OSC SMD2520(JYOD2)



## TAPE AND REEL DIMENSIONS

